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IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF :  
ERIC J STRANG : EXAMINER: SIEK, VUTHE  
SERIAL NO: 10/673,506 :  
FILED: SEPTEMBER 30, 2003 : GROUP ART UNIT: 2825  
FOR: SYSTEM AND METHOD FOR :  
USING FIRST-PRINCIPLES SIMULATION :  
TO ANALYZE A PROCESS PERFORMED :  
BY A SEMICONDUCTOR PROCESSING :  
TOOL

REPLY BRIEF UNDER 37 CFR 41.41

COMMISSIONER FOR PATENTS  
ALEXANDRIA, VIRGINIA 22313

SIR:

This is a Reply Brief to the Examiner's Answer dated October 31, 2008.

OK TO ENTER  
V.S.  
5/12/09